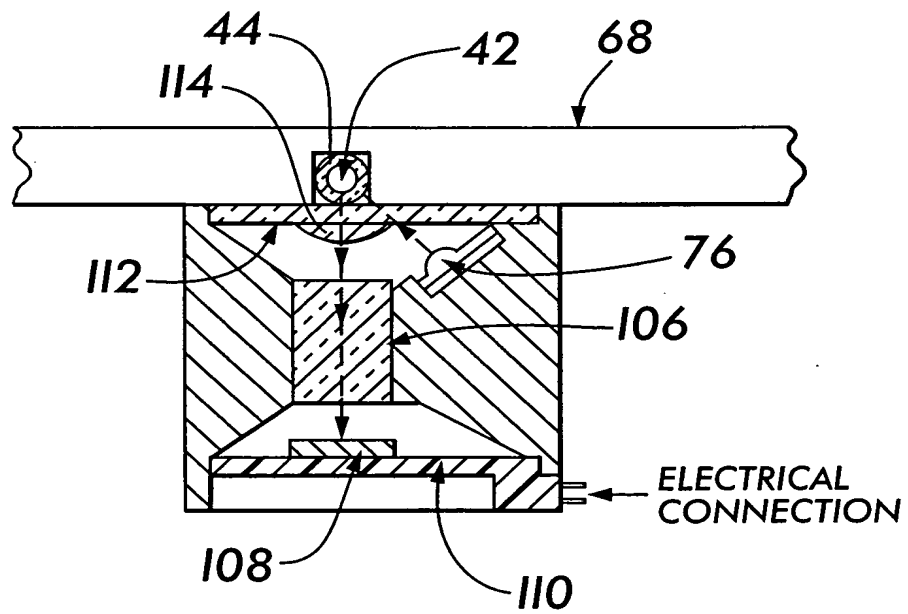
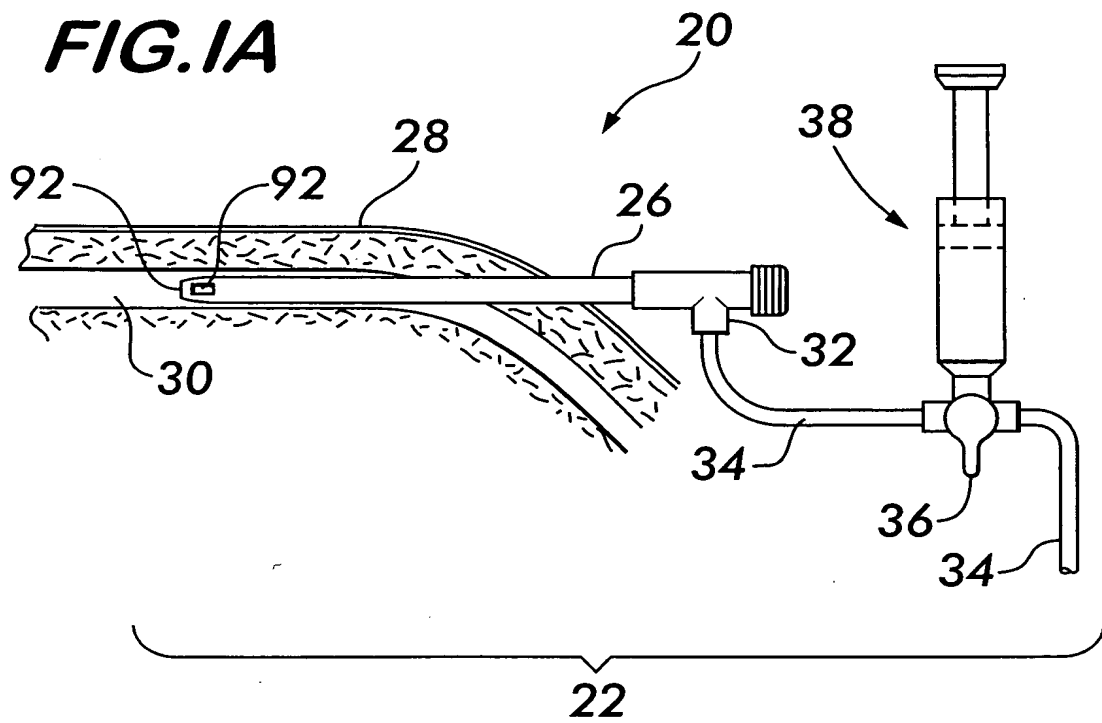
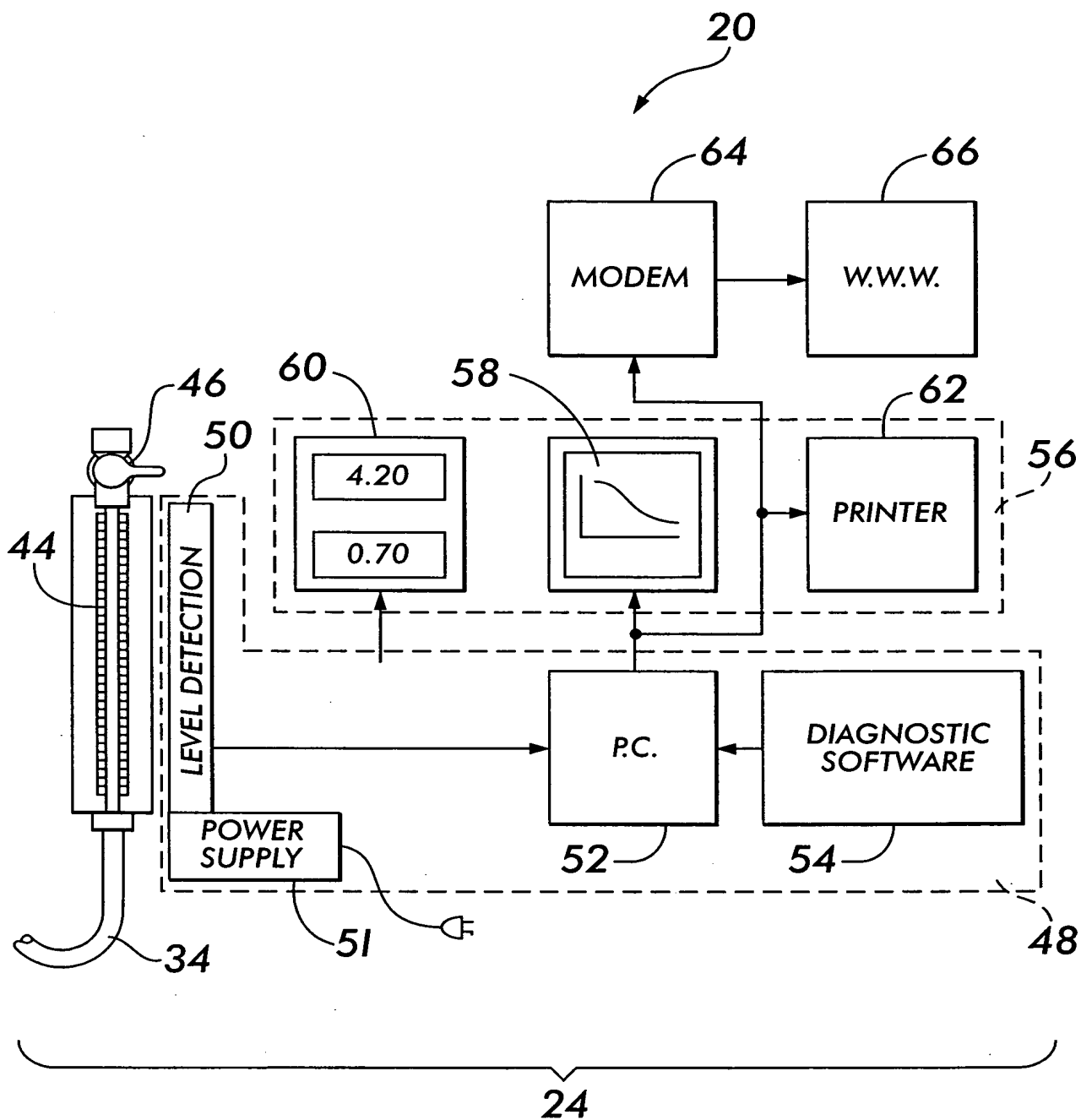


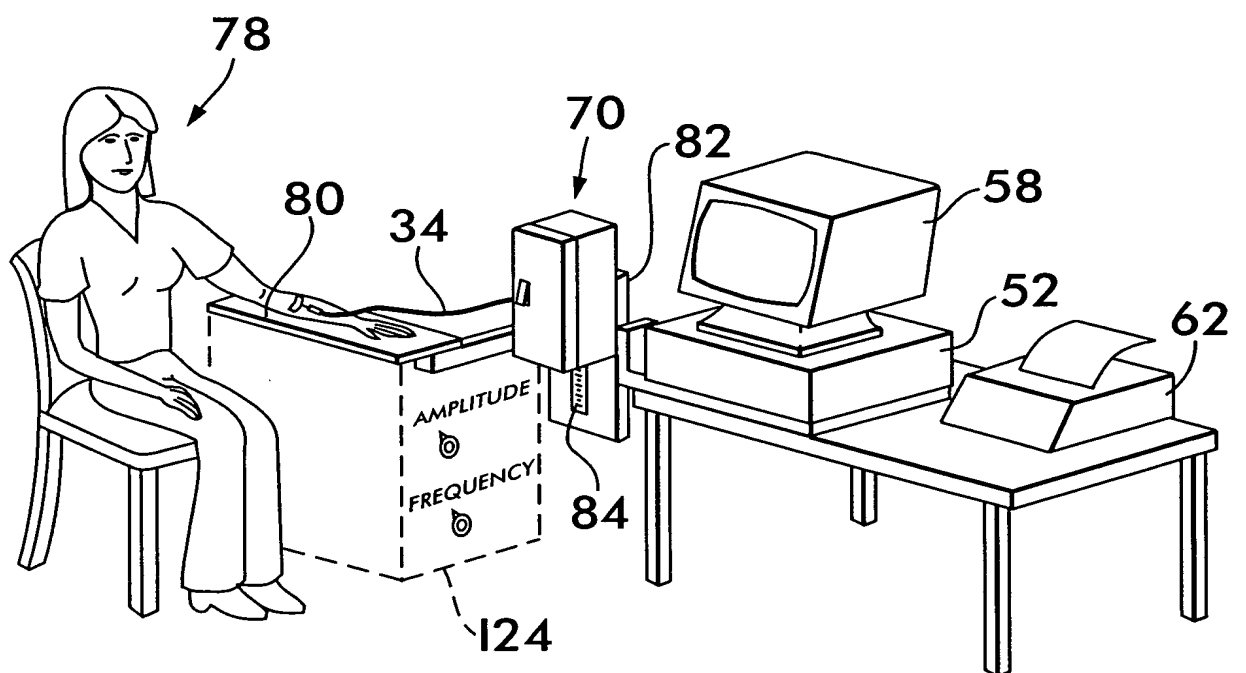
**FIG. 1A****FIG. 12**

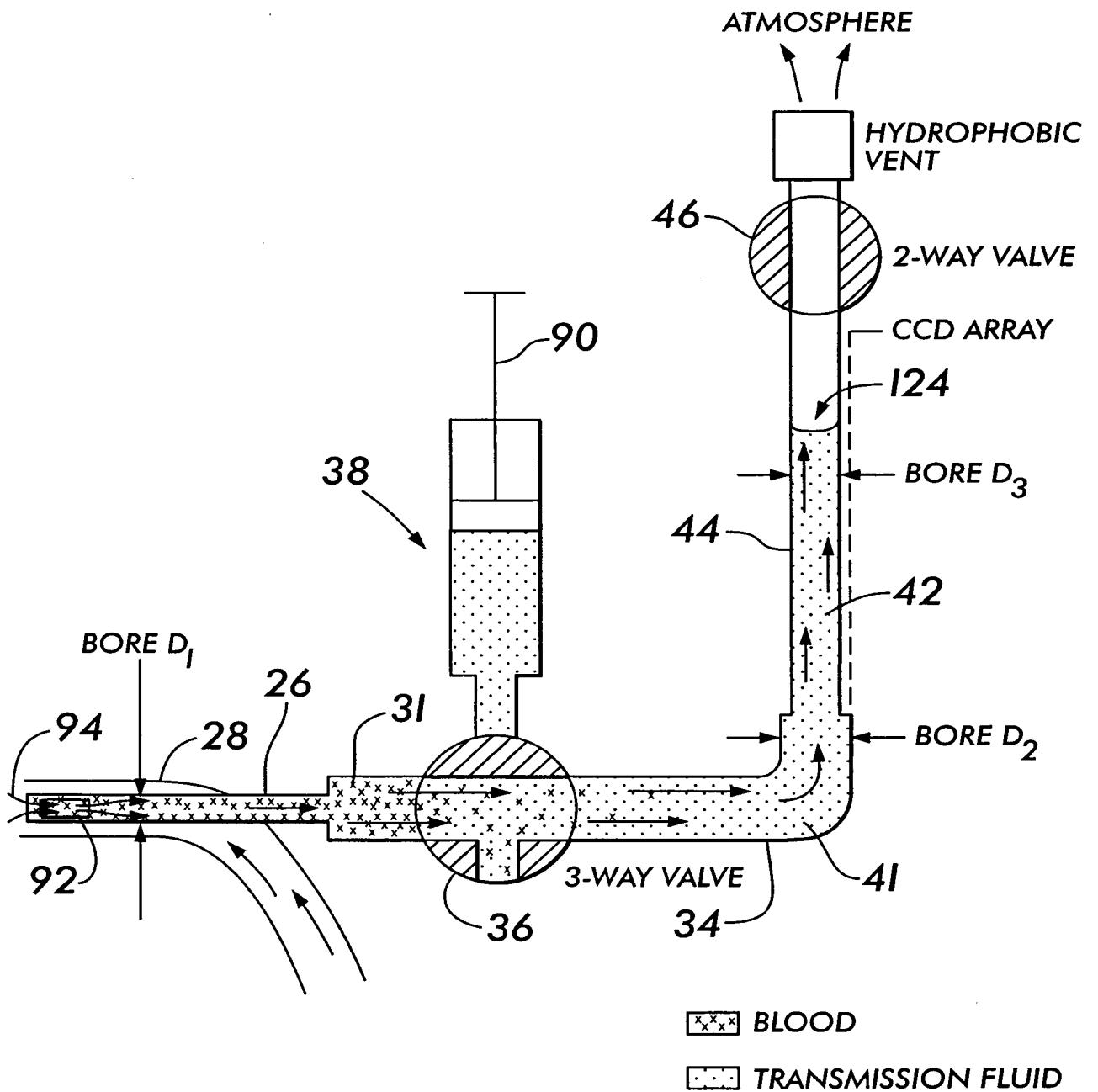


**FIG.1B**



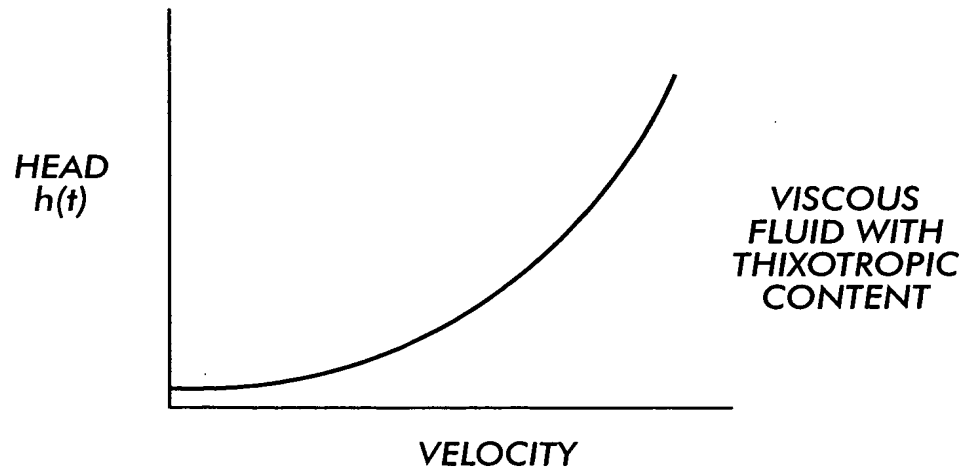
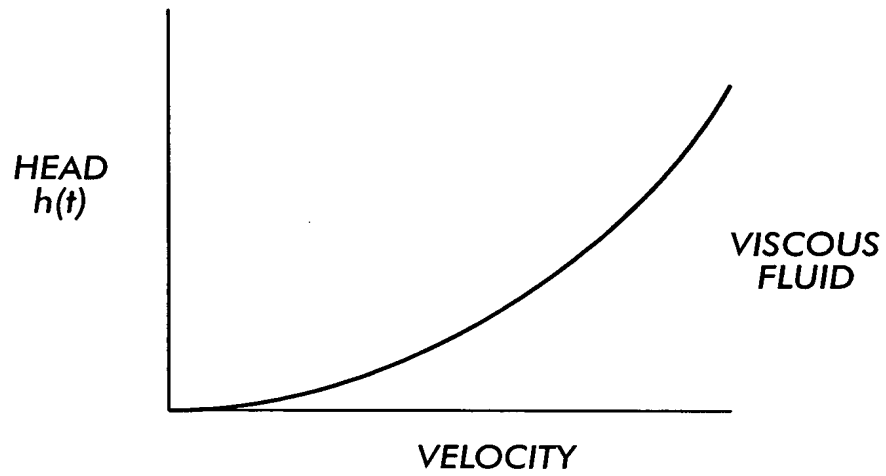
**FIG. 2B**



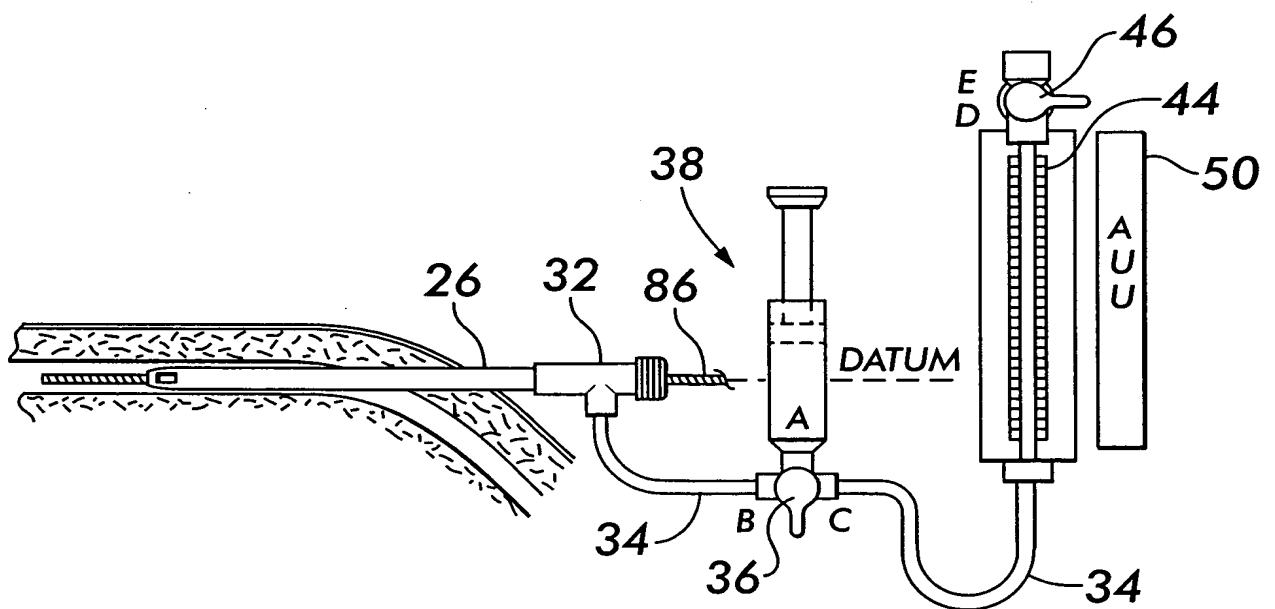
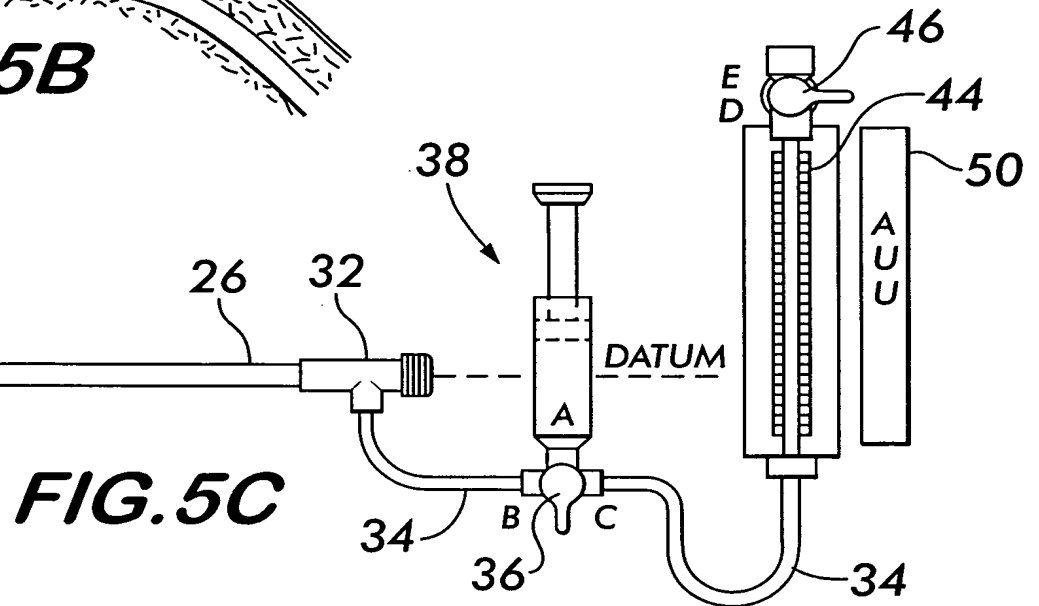
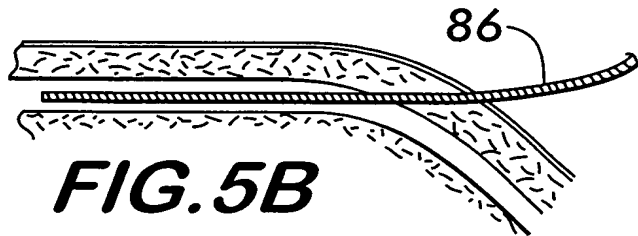
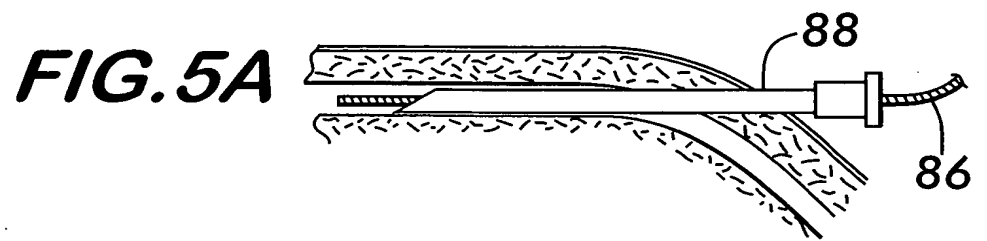


**FIG. 3**

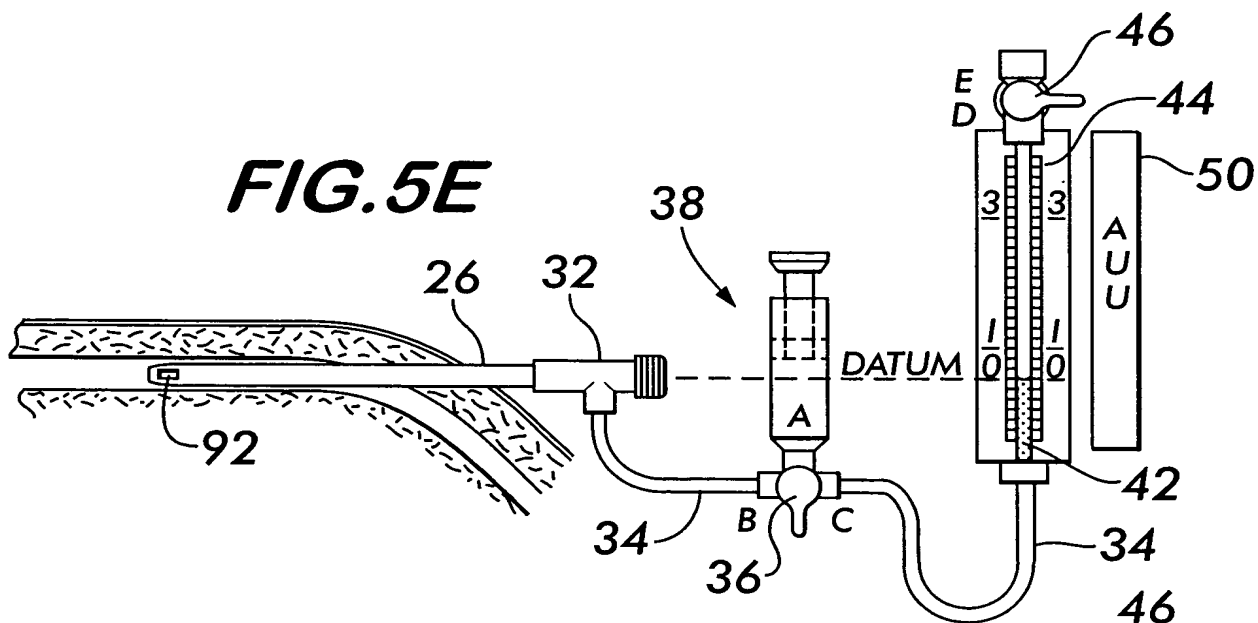
**FIG.4**



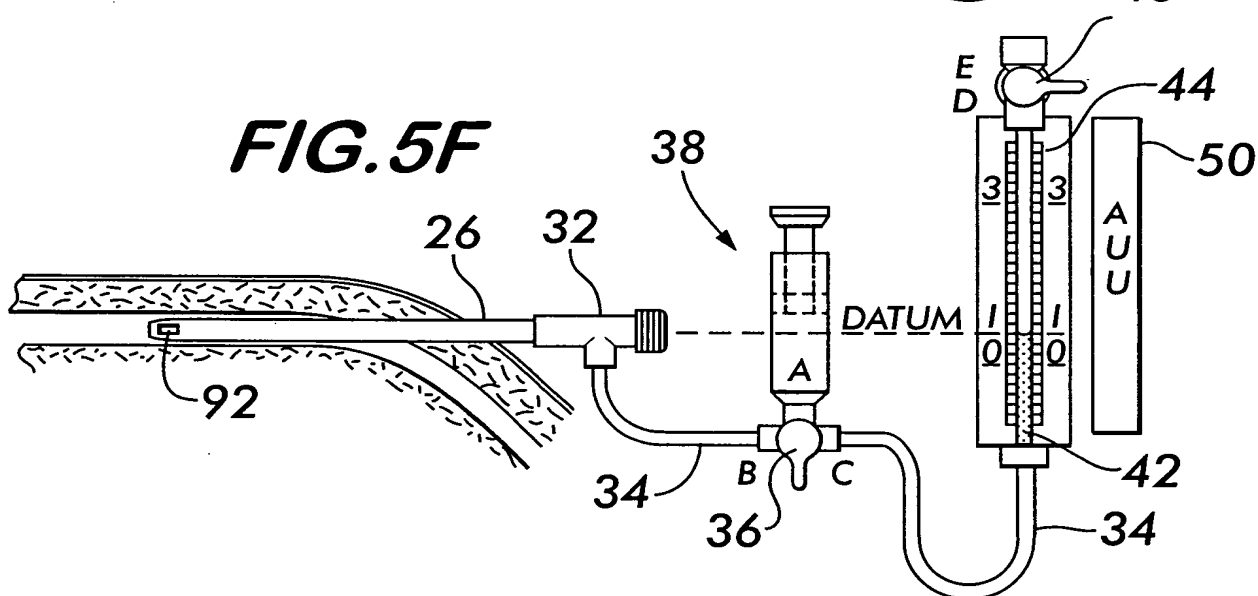
**FIG.14**



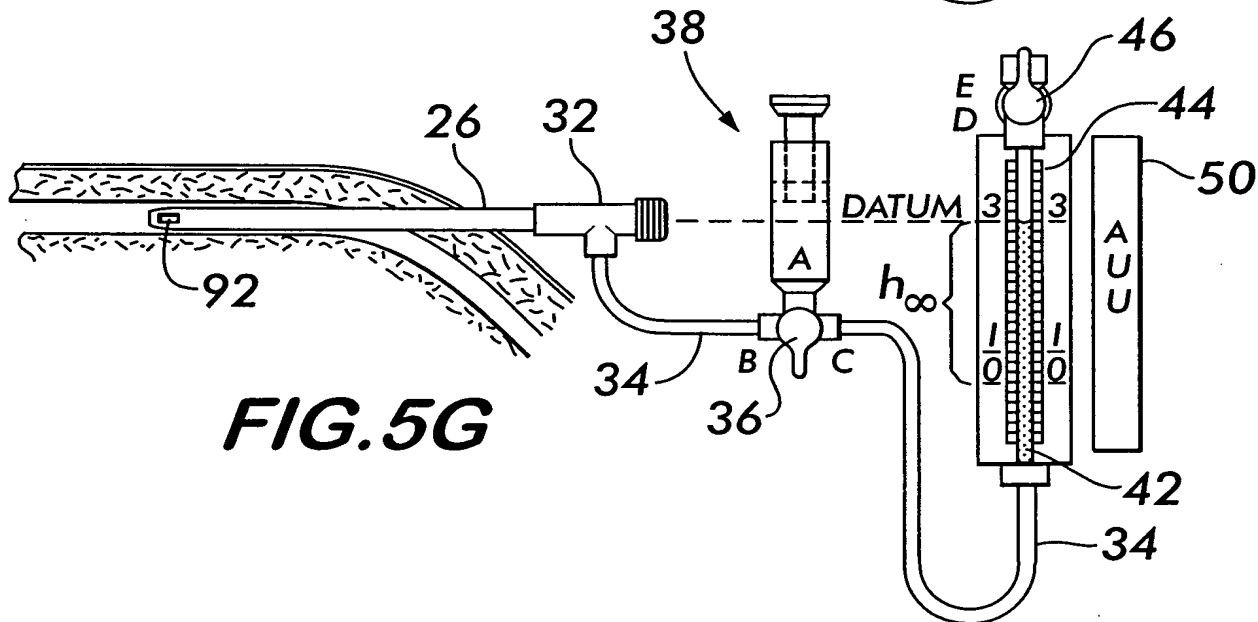
**FIG. 5E**



**FIG. 5F**

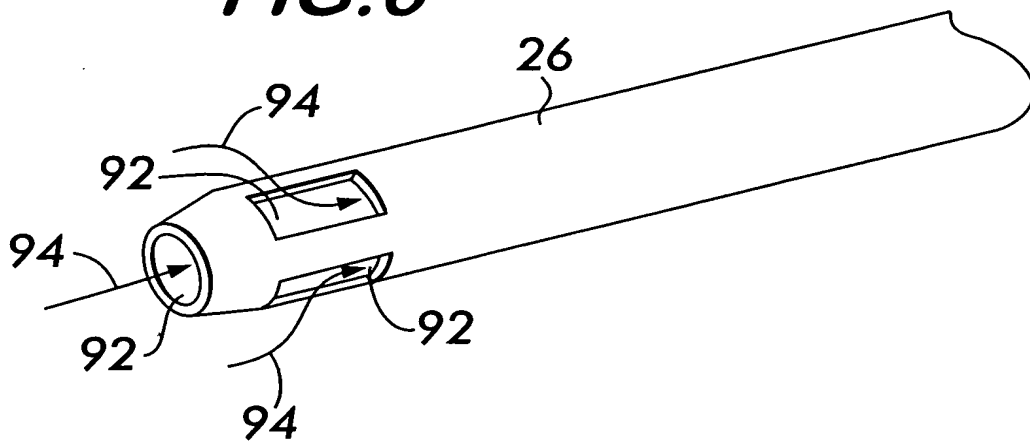


**FIG. 5G**

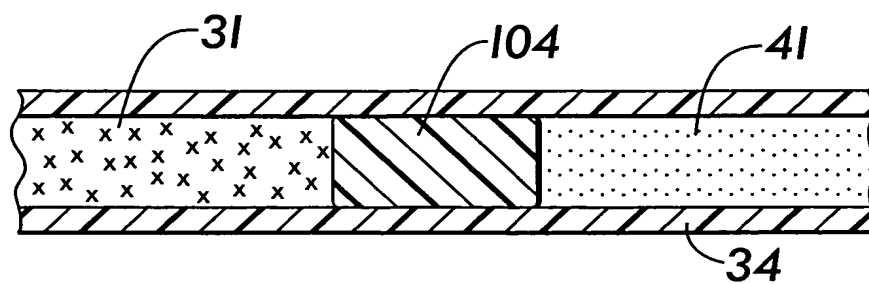
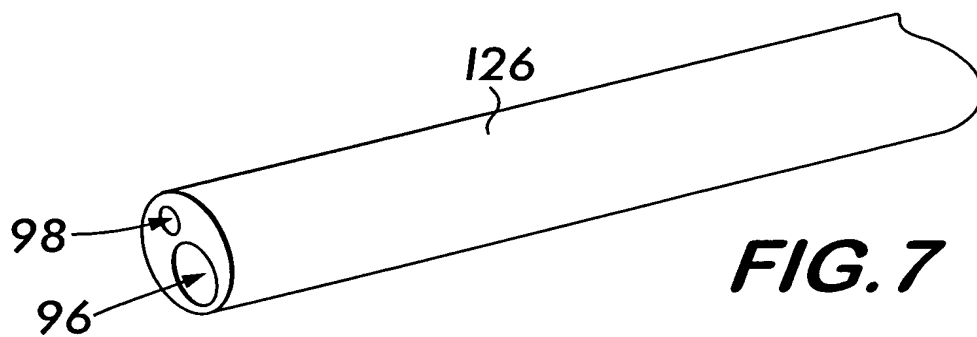




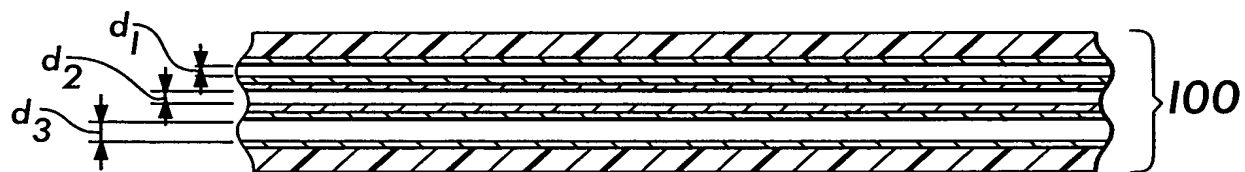
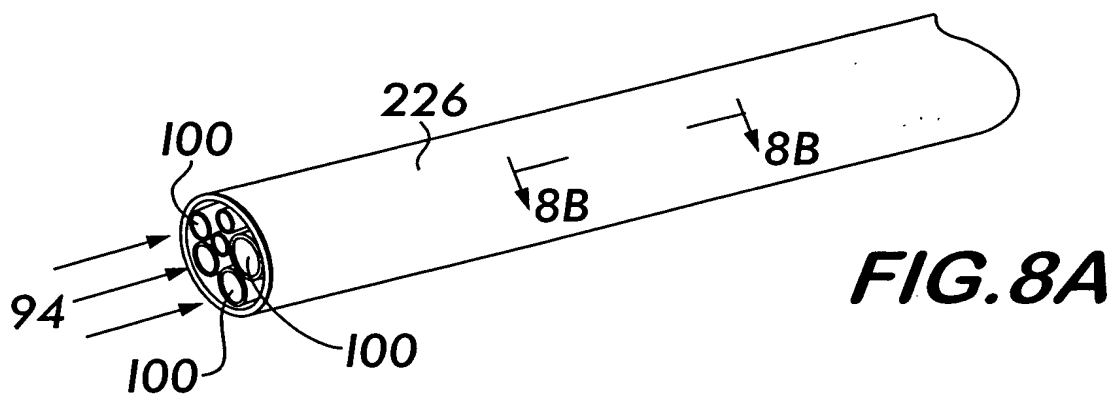
**FIG. 6**



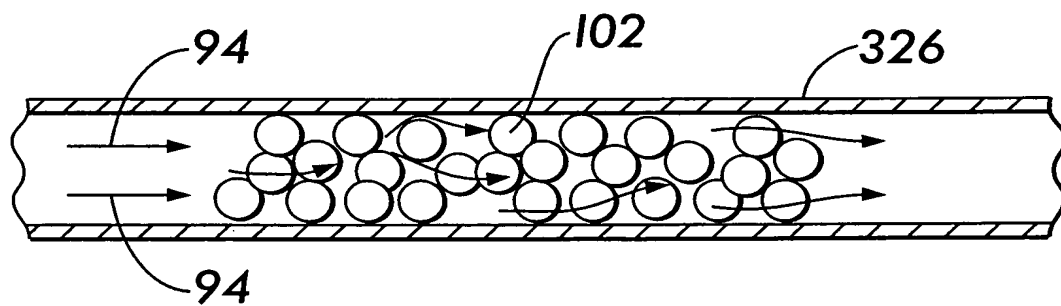
**FIG. 7**



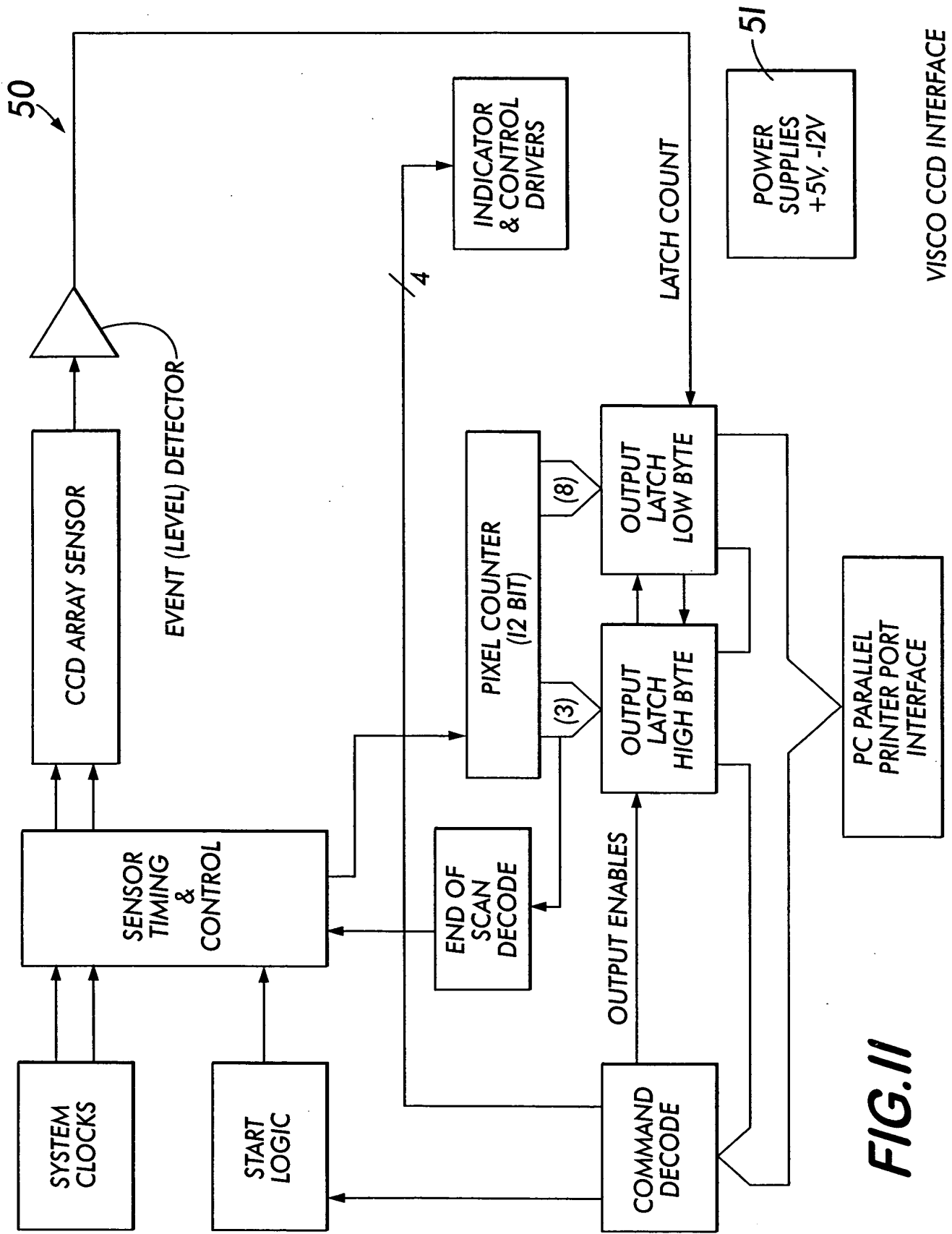
**FIG. 10**



**FIG. 8B**



**FIG. 9**



**FIG. 11**

VISCO CCD INTERFACE

FIG. 13

